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- ☐ 5. **A parameterized low power design for the variable-length discrete Fourier using dynamic pipelining** Jiun-In Guo; Chih-Da Chien; Chien-Chang Lin; Circuits and Systems, 2003. ISCAS '03. Proceedings of the 2003 International Volume 5, 25-28 May 2003 Page(s):V-293 - V-296 vol.5 Digital Object Identifier 10.1109/ISCAS.2003.1206259 [AbstractPlus](#) | Full Text: [PDF](#)(399 KB) IEEE CNF

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